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3 **Realization of a mixed-mode neural coprocessor for signal processing**
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5 **Design of a hybrid digital-analog neural co-processor for signal processing**

Romariz, A.R.S.; Ferreira, P.U.A.; Campelo, J.V., Jr.; Graciano, M.L., Jr.; da Costa, J.C.;

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Clarke, J.A.;
Computational Science and Engineering, IEEE [see also Computing in Science & Engineering] , Volume: 4 , Issue: 1 , Jan.-March 1997
Pages:55 - 62

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4 **Emulation of 3-phase power for study of multi phase instrumentation**
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5 **Simulating shared memory in message passing model**
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